

Amendments to the Claims

Please add claims 16-25.

- 1-15. (canceled)
16. (canceled)
17. (original) A memory system, comprising:
- a processor; and
- a memory bank having data blocks, comprising:
- a system table accessible by the processor to store data about the data blocks;
- and
- at least one erase and write table accessible by the processor to store erase and write time data for the data blocks.
18. (canceled)
19. (canceled)
20. (currently amended) The memory system of claim ~~16~~ 17 wherein the erase and write table also stores data about a total number of bytes for each storage operation.
21. (currently amended) The memory system of claim ~~16~~ 17 wherein a the table also stores number of erasures for each data block.
22. (currently amended) The memory system of claim ~~16~~ 17, wherein the system table is stored in a data block.
23. (currently amended) The memory system of claim ~~16~~ 17, wherein the erase and write table is stored in a data block.
24. (currently amended) The memory system of claim ~~16~~ 17, wherein the data blocks are organized into data banks and the system table and the erase and write table are in the same data bank.

25. (currently amended) The system of claim ~~16~~ 17, wherein the data blocks are organized into data banks and the system table and the erase and write table are in different data banks.